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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/080,037	02/19/2002	Spencer M. Gold	SMQ-088/P6549	8597

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BOSTON, MA 02109

EXAMINER
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VERBITSKY, GAIL KAPLAN

ART UNIT	PAPER NUMBER
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2859

DATE MAILED: 08/05/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/080,037	GOLD ET AL.	
	Examiner Gail Verbitsky	Art Unit 2859	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 09 July 2003.

2a) This action is **FINAL**.                    2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-31 is/are pending in the application.

4a) Of the above claim(s) 17-31 is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-11 is/are rejected.

7) Claim(s) 12-14 is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some \* c) None of:

- Certified copies of the priority documents have been received.
- Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
- Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

#### Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2.

4) Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.

5) Notice of Informal Patent Application (PTO-152)

6) Other: \_\_\_\_\_.

**DETAILED ACTION**

**Restriction/Election**

1. Applicants' election without traverse of Invention I, Group I, claims 1-16, in paper # 5, is hereby acknowledged. The Examiner withdraws non-elected claims 17-31 from further consideration.

***Claim Objections***

2. Claim 12 objected to because of the following informalities: "counter register" in line 10 lacks antecedent basis. Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-2, 9-11, 15-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Iwama (U.S. 4658407).

Iwama discloses in Figs. 1-2 a thermometer comprising a reference (first) oscillating circuit 20 which outputs first (reference) oscillating signal whose frequency (first frequency value) does not depend on temperature, and a thermometric (second) oscillating circuit 10 which outputs a second (temperature dependent) oscillating signal whose frequency (second frequency value) depends on temperature. Iwama also

discloses two counters (first/ temperature dependent) 12 and (second/ reference) 22. The (reference) counter 22 counting oscillations of the reference oscillator 20 and outputting an overflow (reaching a predetermined value) signal to discontinue the oscillation of the thermometric (second) oscillator 10 after a prescribed time to take temperature measurements (col. 3, lines 7-11). A controlling circuit 24 determines if the temperature reaches its maximum value, and resets the counter 22 if not. Thus, production of the thermometric (second) oscillator 10 is inhibited (halted) by the first counter when the reference oscillator/ counter reached its overflow (predetermined) value. A comparison circuit (detector circuit) 16 detects when (detects the edge) a reset signal should reset to the second counter.

5. Claims 1-2, 9, 15-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Fujikawa et al. (U.S. 5626425) [hereinafter Fujikawa].

Fujikawa discloses in Fig. 2 a device comprising an oscillator circuit having a reference oscillator 1 to produce a first reference oscillating frequency signal, a second temperature dependent oscillator 3 to produce a second temperature dependent frequency signal. The device also comprises a temperature counter (second) 6 for measuring the output signal from the temperature sensitive oscillator 3 and, along with a data hold unit 7 holding/ halting the maximum value of said signal to produce a temperature count on the basis of the reference oscillating signal. A first counter (reference control generator) 2 receives the reference oscillating frequency signals and outputs a carrier signal (predetermined value).

***Claim Rejections - 35 USC § 103***

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 3-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama in view of the Prior Art by Woodman, Jr. (U.S. 5832048) [hereinafter Woodman].

Iwama discloses the device as stated above in paragraph 4.

Iwama does not explicitly teach that the reference oscillator circuit comprises a VCO controlled by a temperature *independent* voltage source.

Woodman discloses in Fig. 2 a device in the field of applicant's endeavor comprising a voltage regulator (A/D, arithmetic FFT and D/A) and a voltage controlled oscillator generating a (first) oscillating signal based on a temperature independent voltage source (reference oscillator).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the device, disclosed by Iwama, so as to have the reference oscillator comprised a VCO controlled by a temperature independent voltage source, as taught by Woodman, because both of them are alternate types of oscillators which will perform the same function of providing an oscillating reference frequency, if one is replaced with the other.

8. Claims 3-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama in view of Fry (6362699).

Iwama discloses the device as stated above in paragraph 4.

Iwama does not explicitly teach that the reference oscillator circuit comprises a VCO controlled by a temperature *independent* voltage source.

Fry discloses in Fig. 2 a device in the field of applicant's endeavor comprising a temperature independent voltage source 28, 29, 30, voltage regulator 18,19, 20 and a voltage controlled (tunable) oscillator 16 generating a (first) oscillating signal based on the temperature independent voltage source.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the device, disclosed by Iwama, so as to have the reference oscillator comprised a VCO controlled by a temperature independent voltage source, as taught by Fry, because both of them are alternate Types of oscillators which will perform the same function of providing an oscillating reference frequency, if one is replaced with the other.

9. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama in view of Hodate (U.S. 5193387).

Iwama discloses the device as stated above in paragraph 4.

Iwama does not explicitly teach that the temperature dependent oscillator circuit comprises a VCO controlled by a temperature *dependent* voltage source.

Hodate discloses a VCO 64 outputting a signal of a frequency proportional to a voltage inputted from a temperature transducer (temperature dependent voltage source) 60.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the device disclosed by Iwama, so as to have the temperature dependent oscillator comprised a VCO controlled by a temperature dependent voltage source, as taught by Cornelius, because both of them are alternate types of oscillators which will perform the same function of providing an oscillating temperature dependent signal if one is replaced with the other.

10. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama and Hodate as applied to claim 5 above, and further in view of Binder (U.S. 5892408).

Iwama and Hodate disclose the device as stated above in paragraph 8.

They do not explicitly a voltage regulator to regulate a temperature dependent voltage source, as stated in claim 6.

Binder discloses in Figs. 4 and 11 a device in the field of applicant's endeavor, the device comprising a voltage regulator 80 to regulate a temperature dependent voltage (col. 16, lines 7-8).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the device disclosed by Iwama and Hodate, so as to add a voltage regulator, as taught by Binder, so as to produce a controlled output temperature dependent signal, in order to improve high accuracy of measurements.

11. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama and Hodate as applied to claim 5 above, and further in view of Pippin (U.S. 5838578).

Iwama and Hodate disclose the device as stated above in paragraph 8.

They do not explicitly disclose a bandgap reference circuit, as stated in claim 8.

Pippin discloses in Figs. 1, 3 a device in the field of applicant's endeavor wherein a temperature dependent voltage source comprises a bandgap reference circuit.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to add a bandgap voltage reference circuit, as taught by Lipp, to the dependent temperature voltage source, disclosed by Iwama and Hodate, so as to provide a stable voltage source and to eliminate/ minimize drift/ noises, in order to improve accuracy of the device.

12. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama and Fry as applied to claims 3-4 above, and further in view of Holmdahl (U.S. 5097198).

Iwama and Fry disclose the device as stated above in paragraph 7.

They do not explicitly disclose an independent voltage regulator/ bandgap reference circuit.

Holmdahl disclose a device having a temperature independent voltage source (generator) such as a bandgap voltage source.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to add a bandgap voltage reference circuit, as taught by Lipp, to the independent temperature voltage source, disclosed by Iwama and Fry, so as to provide a stable voltage source and to eliminate/ minimize drift/ noises, in order to improve accuracy of the device.

13. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Iwama and Fry as applied to claims 3-4 above, and further in view of Lipp (U.S. 4165642).

Iwama and Fry disclose the device as stated above in paragraph 7.

They do not explicitly teach that an independent voltage reference source is/ comprises a bandgap circuit.

Lipp discloses in Fig. 1 a temperature independent voltage source comprising a bangap voltage reference circuit 18.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to add a bandgap voltage reference circuit, as taught by Lipp, to the independent temperature voltage source, disclosed by Iwama and Fry, so as to provide a stable voltage source and to eliminate/ minimize drift/ noises, in order to improve accuracy of the device.

14. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation

under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

***Allowable Subject Matter***

15. Claims 12-14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

16. The prior art made of record and not relied upon considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Verbitsky who can be reached at (703) 306-5473 Monday through Friday 7:30 to 4:00 ET.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-5473.

GKV

Gail Verbitsky  
Patent Examiner, TC 2800

July 22, 2003